

Amendments to the Specification

The paragraph number below refers to the paragraph numbers in Published United States Patent Application No. 2004-0233150.

Please replace the paragraph number [0104] with the following rewritten paragraph:

[0104] For the purposes of the present invention, the term "count stage" or "pulse stage" refers to the computations of whether a digitally controlled pulse width can go up or down at a finite number of points. The evaluation of computations at each of these points isare collectively referred to as a stage. There may be multiple clock cycles on the backplane and multiple operations that go into computing whether the pulse goes up or down for a given stage.

Please add the following new paragraph after paragraph [0147]:

For the purposes of the present invention, the term "pulse width" refers to the duration or length of a pulse. One example of how pulse widths may be controlled in accordance with one embodiment of the present invention is illustrated in FIG. 4. As can be seen in FIG. 4, the output pulse width for each pixel value is different. For example, the output pulse width for pixel value 2 is twice as great as the output pulse width for pixel value 1, the output pulse width for pixel value 3 is three times as great as the output pulse width for pixel value 1, *etc.* A given "pulse width" is the duration in time between a single up and then down transition or a single down and then up transition.

Please replace the paragraph number [0252] with the following rewritten paragraph:

[0252] A dual counting with deduction process in accordance with an embodiment of the present invention is shown in FIG. 10, which illustrates a 4/4 split example. Time is shown starting at the LS-bits and continuing through the MS count. The diagram shows the MS-Counting intervals with the LS-Counting intervals expanded below it for clarity. Shown on the diagram are when bits can free-up and are no longer needed for the pulse generation

process. Shown below the timing diagram of FIG. 10 is how the pulse width expand
expands with increasing pixel values.